



# External Research Funding

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# A Broad Portfolio

Semiconductor Research Corporation  
– Industry/Govt

National Nanotech Initiative (NNI) and  
NanoElectronics Research Institute  
(NRI) – Industry/Govt

Semiconductor Technology Council

SRC/MARCO – Industry/Govt

Research Council



- Process
- Devices
- CAD
- Nanotech
- Manufacture



- Circuits
- Architecture
- Communication
- Systems
- Applications

⇒ Heavy use of Consortium Model (shared investment and govt leverage)

⇒ Focused on long range technical challenges

# Research Council Mission

- Support long-term research relevant to Intel's future
- Build and Strengthen ties between Intel and research community
- Support mutually beneficial technical collaboration and interchange
- Train researchers who may become technical leaders at Intel or elsewhere (e.g. the universities)



# Research Council Support Areas

Architecture (Circuits, uArchitecture, CAD, Programming Systems)

Communication (Networking, Optics, Wireless,...)

Systems (Platform, M-cores, Scalable Architecture, ...)

Applications (Mobility, User Interface, Health, Community)

... and some HVM and STC

# Getting Research Council Support

Philosophy: “Pre-couple projects for tech transfer”

- No RFP's
- Every project has a internal Intel Sponsor (technical advocate)
- Sponsor acts as advocate (internal) and connects to the research activity intellectually

## Typical Process

- Build relationship with Intel Researchers/Technical Leaders (conferences, Intel Lablets, ...)
- PI works with sponsor to draft a proposal that addresses a research problem of direct interest to Intel
- Sponsor takes the funding proposal to the research council



# Typical Research Council Grant

3 years, renewed annually

~60-80K / year

Gift or other contracting mechanism

Success:

- High quality work
- Tech transferred into Intel / influences direction
- Students come and work at Intel
- Students graduate, become professors, work on relevant problems

*Thank You!*

